**MuffaKham Jah College of Engineering and Technology**

**Electronics and Communication Engineering Department**

**AICTE-4th SEMESTER SECTION- A and B**

**SUBJECT: COMPUTER ORGANIZATION AND ARCHITECTURE CODE:PC 404 EC**

**CLASS TEST-2 AY:2022-23**

**NOTE:**

**1. ANSWER ALL QUESTIONS FROM PART-A**

**2. ANSWER ANY TWO QUESTIONS FROM PART-B**

**PART-A**

**1. Explain control word [CO3] [2M]**

**2. Draw the timing diagram of a Strobe control method of I/O interface. [CO4] [2M]**

**3. For a given following expression write reverse polish notation. (A+B) \*[C\*(D+E) +F] [CO4] [2M]**

**PART-B**

**4. Explain Zero-address, One-address, Two-address and Three-address Instructions based   
 on various types of CPU organization. [CO3] 7M]**

**5. Explain the operation of daisy chaining method of priority interrupt. [CO4] [7M]**

**6. Explain in detail with neat diagram memory hierarchy in a computer system.[CO5] [7M]**

**Course Coordinator**

**Mr. Mohd. Zakir Hussain**

**Asst.Prof., ECED.**

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| **Class Test Q.No.** | **1** | **2** | **3** | **4** | **5** | **6** |
| **Marks (Xi)** | **2** | **2** | **2** | **7** | **7** | **7** |
| **Bloom’s Score (Si)** | **3** | **4** | **2** | **4** | **3** | **4** |
| **Xi\*Si** | **6** | **8** | **4** | **28** | **21** | **28** |

**BI=4.75**